

ABSTRACT OF THE DISCLOSURE

A drive control circuit supplies a gate voltage so that display quality is not degraded even in a case where a vertical scanning frequency or a horizontal scanning frequency is changed. The circuit includes a timing controller for detecting a change of a horizontal scanning frequency, a gate voltage generating circuit for generating two kinds of gate-on voltages V_a and V_b ($V_a < V_b$), and a switch for outputting one of the gate-on voltages V_a and V_b from the gate voltage generating circuit in accordance with an output of the timing controller. The timing controller includes a counter for counting the number of clocks for one horizontal period, and a comparator for comparing a count result with a threshold value. When the horizontal scanning frequency is in a normal state, the low gate-on voltage V_a is outputted, and when the horizontal scanning frequency exceeds a predetermined threshold value, that is, the count value falls below a threshold value, the high gate-on voltage V_b is outputted.